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DATE MAILED: 03/21/2005

APPLICATION NO.	F	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
10/720,466		11/24/2003	Anthony Correale JR.	YOR920030373US1	4073		
33233	7590	03/21/2005		EXAM	EXAMINER		
		CHARLES W. PI EEN DRIVE	CHANG, DANIEL D				
SUITE 100	MAN UK	EEN DRIVE		ART UNIT	PAPER NUMBER		
RESTON, V	A 2019	)	2819				

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)	
•	10/720,466	CORREALE ET AL.	an
Office Action Summary	Examiner	Art Unit	(010
•	Daniel D. Chang	2819	
The MAILING DATE of this communication app	<u> </u>		s
Period for Reply			-
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this commun O (35 U.S.C. § 133).	nication.
Status			
1) Responsive to communication(s) filed on 24 No.	ovember 2003.		
	action is non-final.		
3) Since this application is in condition for allowar	nce except for formal matters, pro	secution as to the mer	rits is
closed in accordance with the practice under E	ix parte Quayle, 1935 C.D. 11, 45	3 O.G. 213.	
Disposition of Claims			
4) Claim(s) 1-30 is/are pending in the application.			
4a) Of the above claim(s) is/are withdraw			
5) Claim(s) is/are allowed.			
6)⊠ Claim(s) <u>1-30</u> is/are rejected.			
7) Claim(s) is/are objected to.			
8) Claim(s) are subject to restriction and/or	r election requirement.		
Application Papers			
9)⊠ The specification is objected to by the Examine	r		
10) ☐ The drawing(s) filed on 24 November 2003 is/a		ed to by the Examiner	
Applicant may not request that any objection to the			•
Replacement drawing sheet(s) including the correcti	* ,	, ,	121(d)
11) The oath or declaration is objected to by the Ex	• • • • • • • • • • • • • • • • • • • •		
Priority under 35 U.S.C. § 119			
<u> </u>		(4) (6)	
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:	phonty under 35 U.S.C. § 119(a)	-(a) or (t).	
,,,	s have been received		
<ul><li>1. Certified copies of the priority documents</li><li>2. Certified copies of the priority documents</li></ul>		on No	
3. Copies of the certified copies of the prior	• •		10
application from the International Bureau		u III tilis Ivational Stay	l <b>C</b>
* See the attached detailed Office action for a list of	• • • • • • • • • • • • • • • • • • • •	d.	
Attachment(s)	<b></b> -	(070,440)	
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	4)		
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)	5) 🔲 Notice of Informal P	atent Application (PTO-152)	•
Paper No(s)/Mail Date <u>11/24/03</u> .	6)		

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# Specification

The disclosure is objected to because of the following informalities: on page 1, lines 5 and 8, serial numbers need to be filled out. Appropriate correction is required.

#### Claim Objections

Claim 30 is objected to because of the following informalities: it appears that claim 30 should depend on 19 instead of 18 and "Vddl" appears to be --Vddh--. Appropriate correction is required.

# Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 11, 17, and 29 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Regarding claims 11 and 29, the recitation, "said CMOS inverter" lacks antecedent basis.

Regarding claim 17, the recitation, "the FET" lacks antecedent basis.

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

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(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-5, 7-10, 12-15, 18-23, and 25-28 are rejected under 35 U.S.C. 102(b) as being clearly anticipated by Feller (EP 125,733 A1).

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 6, 16, and 24 are rejected under 35 U.S.C. 103(a) as being unpatentable over Feller.

Feller clearly discloses all the features of the claimed invention but does not disclose that the at least one diode connected NFET is a plurality or a pair of series connected NFET diodes.

However, it is well known in the art that when more voltage drop is desired, more diodes are connected in series. Therefore, it would have been obvious at the time the invention was made to an ordinary skilled in the art to have provided NFET diode (N4) of Feller with more NFET diode(s) in series in order to provide more voltage drops.

#### Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

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Morris (US 5,304,867) discloses TTL-to-CMOS input buffer with high speed and low

power.

Foss (US 4,786,830) discloses CMOS input buffer circuit for TTL signals.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Daniel D. Chang whose telephone number is (571) 272-1801.

The examiner can normally be reached on Monday through Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Michael J. Tokar can be reached on (571) 272-1812. The fax phone number for the

organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Daniel D. Chang

Primary Examiner

Art Unit 2819

DANIEL CHANG PRIMARY EXAMINER

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